

A demodulator of digital terrestrial broadcast or the like for transmitting coded digital video and audio information in packet form comprises a synchronous code pattern detecting circuit for detecting the segment synchronous code pattern from the most significant bit signal of the reception packet data, a symbol number counter circuit for counting the number of symbol data in the reception packet data, a synchronism detection establishing circuit for judging the true segment synchronous code pattern by obtaining the segment synchronous code pattern from the synchronous code pattern detecting circuit when the symbol number counter circuit finishes counting of a specified number, and a synchronism detection protection counter circuit for detecting and establishing the segment synchronous signal in the reception data from the output of the synchronous code pattern detecting circuit and count-up of specified number of the symbol number counter circuit. In this constitution, even in an inferior environment for receiving broadcast such as deterioration of C/N of signal due to weak electric field, or strong ghost or multipath characteristic of terrestrial waves, the digital broadcast demodulator capable of processing packet synchronism detection, AGC, and clock regeneration stably and precisely is presented.